

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,948,096 B2  
DATED : September 20, 2005  
INVENTOR(S) : Parvathala, Praveen K.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9,

Line 43, after "comprises" delete ";" and insert -- : --, therefor.

Column 10,

Line 51, after "tests" insert -- ; --.

Line 67, after "following" delete ";" and insert -- : --, therefor.

Column 11,

Line 25, after "microprocessor" insert -- , --.

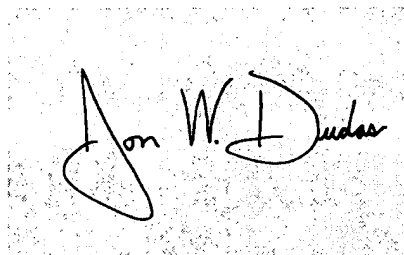
Column 12,

Line 2, after "microprocessor" insert -- , --.

Line 5, delete "memo" and insert -- memory --, therefor.

Signed and Sealed this

Twentieth Day of December, 2005

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular, textured background that appears to be a stamp or seal.

JON W. DUDAS

*Director of the United States Patent and Trademark Office*